

L Number	Hits	Search Text	DB	Time stamp
1	1	5206939.pn.	USPAT; US-PGPUB	2001/09/05 16:35
2	52996	(microprocessor or cpu or processor) near10 interfac\$4	USPAT; US-PGPUB	2001/09/05 17:14
3	29386	(memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4	USPAT; US-PGPUB	2001/09/05 16:41
4	11886	((microprocessor or cpu or processor) near10 interfac\$4) with ((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4 )	USPAT; US-PGPUB	2001/09/05 16:41
5	32786	(microprocessor or cpu or processor) near6 (chip or asic or integrated)	USPAT; US-PGPUB	2001/09/05 16:46
6	3368	((microprocessor or cpu or processor) near10 interfac\$4) with ((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4 )) and ((microprocessor or cpu or processor) near6 (chip or asic or integrated))	USPAT; US-PGPUB	2001/09/05 16:45
7	1217	((microprocessor or cpu or processor) near6 (chip or asic or integrated)) near15 (buffer or queue or fifo)	USPAT; US-PGPUB	2001/09/05 16:48
8	75920	(select\$3 or multiplex\$3) near15 (port or path)	USPAT; US-PGPUB	2001/09/05 16:50
9	6	((microprocessor or cpu or processor) near6 (chip or asic or integrated)) near15 (buffer or queue or fifo)) with ((select\$3 or multiplex\$3) near15 (port or path))	USPAT; US-PGPUB	2001/09/05 17:11
10	0	l12 near10 ((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4 )	USPAT; US-PGPUB	2001/09/05 17:13
11	2927	((microprocessor or cpu or processor) near10 interfac\$4) with ((microprocessor or cpu or processor) near6 (chip or asic or integrated))	USPAT; US-PGPUB	2001/09/05 17:13
12	935	((microprocessor or cpu or processor) near10 interfac\$4) with ((microprocessor or cpu or processor) near6 (chip or asic or integrated))) and ((select\$3 or multiplex\$3) near15 (port or path))	USPAT; US-PGPUB	2001/09/05 17:13
13	24981	(microprocessor or cpu or processor) with interfac\$4 with memory	USPAT; US-PGPUB	2001/09/05 17:15
14	583	((microprocessor or cpu or processor) near10 interfac\$4) with ((microprocessor or cpu or processor) near6 (chip or asic or integrated))) and ((select\$3 or multiplex\$3) near15 (port or path))) and ((microprocessor or cpu or processor) with interfac\$4 with memory)	USPAT; US-PGPUB	2001/09/05 17:16
15	1561	710/129-130.ccls.	USPAT; US-PGPUB	2001/09/05 17:18
16	533	710/129-130.ccls. and ((microprocessor or cpu or processor) with interfac\$4 with memory)	USPAT; US-PGPUB	2001/09/05 17:18
17	23	(710/129-130.ccls. and ((microprocessor or cpu or processor) with interfac\$4 with memory)) and ((microprocessor or cpu or processor) near6 (chip or asic or integrated)) near15 (buffer or queue or fifo))	USPAT; US-PGPUB	2001/09/05 17:18
18	9	((710/129-130.ccls. and ((microprocessor or cpu or processor) with interfac\$4 with memory)) and ((microprocessor or cpu or processor) near6 (chip or asic or integrated)) near15 (buffer or queue or fifo))) and ((select\$3 or multiplex\$3) near15 (port or path))	USPAT; US-PGPUB	2001/09/05 17:46
19	15077	interfac\$3 near6 (chip or asic or integrated)	USPAT; US-PGPUB	2001/09/05 17:51
20	228	(interfac\$3 near6 (chip or asic or integrated)) near (buffer or fifo or queue)	USPAT; US-PGPUB	2001/09/05 17:52

21	858	(interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)	USPAT; US-PGPUB	2001/09/05 17:52
22	2711	(interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)	USPAT; US-PGPUB	2001/09/05 17:53
23	2321	(interfac\$3 near6 (chip or asic or integrated)) near10 memory	USPAT; US-PGPUB	2001/09/05 17:53
24	739	((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) with ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)	USPAT; US-PGPUB	2001/09/05 17:54
25	58	((((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) with ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)) and ((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue))	USPAT; US-PGPUB	2001/09/05 17:54
26	15	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) with ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)) and ((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue))) and ((select\$3 or multiplex\$3) near15 (port or path))	USPAT; US-PGPUB	2001/09/05 18:12
27	7	sandorfi-miklos\$.in.	USPAT; US-PGPUB	2001/09/05 18:19